

## **Acceptance Test Requirements Matrix for LVT(1)**

March 19, 2015

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3.2.1.1.1.19.2.f		T	Limited to verification of terminal entering into a "Load Complete, Valid Data" state.
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3.2.1.1.1.20.2	Network information exchange	T	
3.2.1.1.1.21.1	Antenna interface signal output	T	
3.2.1.1.1.21.1.b.1		T	Measurement by averaging the peak power of the pulses in a time slot is acceptable.
3.2.1.1.1.21.1.b.3		T	Measurement by averaging the peak power of the pulses in a time slot is acceptable.
3.2.1.1.1.21.1.b.5		T	Measurement by averaging the peak power of the pulses in a time slot is acceptable.
3.2.1.1.1.21.1.c		T	
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3.2.1.1.1.22	TDMA transmission compatibility capability	T	Limited to Exercise and Combat modes only. 100/50 and 100/20 modes not required.

## Attachment I

SSS Section Reference	Title	Verification Method	Notes
3.2.1.1.1.22.1.2	1030/1090 MHz emissions	T	
3.2.1.1.1.22.1.2.a		T	
3.2.1.1.1.24.2	Error rates	T	Limited to benign RF environment only.
3.2.1.1.2 (If applicable)	TACAN mode	T	
3.2.1.1.2.b (If applicable)		T	Limited to 25% transmit and 25% receive TDMA duty cycle only.
3.2.1.1.2.c (If applicable)		T	
3.2.1.1.2.d (If applicable)		T	
3.2.1.1.2.1 (If applicable)		T	Limited to TACAN beacon only.
3.2.1.1.2.1.b (If applicable)		T	Limited to TACAN beacon only.
3.2.1.1.2.1.c (If applicable)		T	Worst case tolerance additions need not be tested.
3.2.1.1.2.1.c.1 (If applicable)		T	
3.2.1.1.2.1.c.6 (If applicable)		T	
3.2.1.1.2.1.1 (If applicable)	Identification	T	
3.2.1.1.2.1.1.a (If applicable)		T	Limited to aural tone only.
3.2.1.1.2.2 (If applicable)	Air-to-air capability	T	Limited to transmit and receive configuration only.
3.2.1.1.2.2.b (If applicable)		T	
3.2.1.1.2.2.b.1 (If applicable)		T	
3.2.1.1.2.2.b.6 (If applicable)		T	
3.2.1.1.2.3.1a (If applicable)	Transmission	T	
3.2.1.1.2.5 (If applicable)	TACAN host information exchange capability	T	Station identification through the data bus interface not required. Verification to be performed through information contained in FOM21.
3.2.1.2.1.1	Data retention capability	T	Verification to be performed consistent with ATP testing. Data retention of 30 days or 48 hours not required.
3.2.1.2.1.3	Cryptovariable erase capability	T	
3.2.3.1	Data bus interface description	T	Limited to variation 1 only.
3.2.3.1.c		T	Limited to variation 1 only.
3.2.3.1.1	General rules	T	Limited to variation 1 only.
3.2.3.1.1.3.8	Bit initiate	T	
3.2.3.1.1.3.9	TDMA NOGO	T	

SSS Section Reference	Title	Verification Method	Notes
3.2.3.1.2	Avionics data bus interface	T	Limited to variation 1 only.
3.2.3.1.2.1	MIL-STD-1553B	T	
3.2.3.2	Suppression signal interface description	T	
3.2.3.2.1.a		T	Limited to signal operation only. Timing parameter measurements not required.
3.2.3.2.1.b		T	Limited to signal operation only. Timing parameter measurements not required.
3.2.3.2.1.c		T	Limited to signal operation only. Timing parameter measurements not required.
3.2.3.2.1.e		T	Limited to signal operation only. Timing parameter measurements not required.
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3.2.3.6 (If applicable)	TACAN interface description	T	To include the PANAVIA interface.
3.2.3.6.c (If applicable)		T	
3.2.3.7.2	Timing signal interface description -mode 2	T	
3.2.3.7.2.1	Time synchronization and input interface	T	
3.2.3.7.2.2	Time synchronization and output interface	T	Verification of synchronization between FOM08 and time strobe pulse not required.
3.2.3.12 (If applicable)	Voice interface description	T	Verification of 2.4 kbps voice interface not required.
3.2.3.12.2 (If applicable)	16 kbps voice analog interface	T	
3.2.3.13.1	SDU zeroize interface	T	Erasure of cryptovariables via a zeroize command from the MUX bus not required.
3.2.3.13.2	Terminal power on interface	T	
3.2.3.13.3	Terminal standby interface	T	
3.2.3.14	Status interface	T	
3.2.3.14.2	Reception detect	T	
3.2.3.14.3	Output power level	T	Limited to signal operation only. Timing parameter measurements not required.
3.2.3.14.5	Advance slot notification interfaces	T	Limited to mode B and signal operation only. Timing parameter measurements not required.
3.2.3.14.7	Active receive indication interface	T	
3.2.3.14.8	IFF emergency switch interface	T	

## Attachment I

SSS Section Reference	Title	Verification Method	Notes
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3.2.4.5	Mounting provisions	T	
3.2.5.2.5	Maintainability design criteria	T	
3.2.5.2.5.i		T	Verify Part Number, Serial Number, status of cumulative operating hours and the number of times the SRU has been switched from off to any other state.
3.2.5.2.6	Built-in tests	T	Verification of no more than one out of every 100 failures detections shall be false alarm" not required. "
3.2.5.2.6.1	Start-up BIT	T	Verification of completion of start-up BIT in fifteen seconds not required.
3.2.5.2.6.2	Operational BIT	T	Verification of the 12 second reporting requirement or the detection requirement not required
3.2.5.2.6.3	Manually initiated BIT	T	
3.3.1.3	Environmental stress screening (ESS)	T	
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